

**Group List of ELEC/CPEG Students with
Final Year Project (FYP)/ Final Year Thesis (FYT) (2022-2023)**

(Last update on 3 Apr 2023)

**Group representative*

Student Name	Project Code	Supervisor (Prof.)	Project Title	Project Type	Communication Tutor (Ms./ Mr.)
KOO, Man Lung*	AS01a-22	Abhishek Kumar SRIVASTAVA	QRLED with high efficiency! (FYP)	FYP	David
LEUNG, Siu Hei					
RAI, Bonil Sampang					
CHENG, Wai Yan*	AS02a-22	Abhishek Kumar SRIVASTAVA	FLC LiDARs with extended field of view (FYP)	FYP	Shaneli
CHU, Chun San					
YUEN, Tsz Kin					
NGAN, Siu Tang*	CK01a-22	Kevin CHAU	Hearing Loss Compensation by Means of a Real-Time, Multi-Band, Wavelet-Based Method (FYP)	FYP	David
LI, Chun Ho					
NG, Chun Ho*	CM01a-22	Mansun CHAN	Action Based Mobile Game Apps for Elderly Training (FYP)	FYP	Shaneli
PANG, Chi Ka					
TSANG, Kin Ming					
CHAN, Sin Wai*	CM01b-22	Mansun CHAN	Action Based Mobile Game Apps for Elderly Training (FYP)	FYP	David
TAM, Ho Lam					
NG, Cheuk Kei					
YANG, Zheng	CM02a-22	Mansun CHAN	Implementation of Neural Network Generated Resistive Memory Models to a Circuit Simulator (FYT)	FYT	Shaneli
WONG, Kwok Ho	CM03a-22	Mansun CHAN	Design and Fabrication of Resistive RAM for AI Circuits (FYT)	FYT	David
MAN, Hoa Ming*	FZ01a-22	Zhiyong FAN	Smart Gas Sensor System for Air Quality Monitoring (FYP)	FYP	Shaneli
CHAN, Pak Ho					
LUI, Chin Hei	GY01a-22	George Jie YUAN	Microcontroller Hardware/Software Design (FYP)	FYP	David
LUK, Wing Tung*	JQ01a-22	Jianan QU	Development of Mobile healthcare Apps (FYP)	FYP	Shaneli
SETO, Cho Chin					
CHENG, Tsun Hei*	JQ01b-22	Jianan QU	Development of Mobile healthcare Apps (FYP)	FYP	David
CHUNG, Cheuk Hei					
LAU, Ching Shun					
TO, Kin Yin*	JQ03a-22	Jianan QU	Biological signals and computer interface (FYP)	FYP	Shaneli
WONG, Ho Chi Hubert					
WONG, Wai Lun					
NG, Hugo*	JQ03b-22	Jianan QU	Biological signals and computer interface (FYP)	FYP	David
CHOW, Cho Kit					

KANG, Tianqu	KBL01a-22	Khaled BEN LETAIEF	Edge AI for 6G wireless communications (FYT)	FYT	Shaneli
WONG, Ching Man	KBL01b-22	Khaled BEN LETAIEF	Edge AI for 6G wireless communications (FYT)	FYT	David
ZHU, Guanhua*	KW02a-22	Wing Hung KI	Power Management IC Design (FYP)	FYP	David
DENG, Dihua					
CAI, Yuxiang*	LH01a-22	Howard LUONG	DESIGN OF RADIO-FREQUENCY INTEGRATED CIRCUITS (FYP)	FYP	Shaneli
XING, Hexiang					
GU, Haiyun	LH01b-22	Howard LUONG	DESIGN OF RADIO-FREQUENCY INTEGRATED CIRCUITS (FYP)	FYP	David
SUN, Lok Tin*	LH03a-22	Howard LUONG	DESIGN OF INTEGRATED VARIABLE-GAIN AMPLIFIERS (FYP)	FYP	Shaneli
TAM, Ho Fai					
WONG, Chi Chit Jabez					
HO, Ho Shing*	LXM01a-22	Xiaomeng LI	Label-efficient deep learning for surgical video analysis (FYP)	FYP	David
HUANG, Jiajun					
MATHANUKRAW, Thanat*	LXM02a-22	Xiaomeng LI	Deep-Learning-based Pipeline for Data Efficient and Weakly Supervised Whole-Slide-level Analysis (FYP)	FYP	Shaneli
BAECK, Hyun Woo					
SOO, Wei Kuang Gabriel					
HO, Wai Lok*	LXM03a-22	Xiaomeng LI	Development of AI-based algorithm for retinal disease screening (FYP)	FYP	David
WONG, Ho Yin					
YIP, King Wang					
DASWANI, Sahil Bharat	LXM03b-22	Xiaomeng LI	Development of AI-based algorithm for retinal disease screening (FYP)	FYP	Shaneli
GUO, Jiarong	LXM04a-22	Xiaomeng LI	Federated Learning with Medical Image (FYT)	FYT	David
TSANG, Yik Wai*	LY01a-22	Levent YOBAS	MIDI-controlled FPGA-based digital music synthesizer (FYP)	FYP	Shaneli
CHEN, Shufan					
SU, Wanying					
CHAN, Tsz Hin*	LY02a-22	Levent YOBAS	FPGA-controlled single refreshable Braille character (FYP)	FYP	David
CHENG, Hei Yeung					
WONG, Dennis Guan Ming					
HO, Ka Chun Ken*	LY02b-22	Levent YOBAS	FPGA-controlled single refreshable Braille character (FYP)	FYP	Shaneli
LAW, Sau Yin					
XIAO, Chenghui					
HA, Yuen Nga*	MR01a-22	Ross MURCH	Net-Zero IoT based Sensing System (FYP)	FYP	David
HUI, Wing Tung					
LEUNG, Hei Yi					
GUPTA, Pranav*	MR02a-22	Ross MURCH	IoT based radio tomographic imaging systems (FYP)	FYP	Shaneli
LEE, Kwun Yin					
CHOW, Hiu Yeung	MS01a-22	Scottie Tsz Yin MAN	Design of Linear Regulator IC Based on Open-Source Design Flow (FYP)	FYP	David
KIM, Seonghyun*	MS03a-22	Scottie Tsz Yin MAN		FYP	Shaneli
KO, Kin Chi					

LEUNG, King Nam			Design of Buck Regulator IC Based on Open-Source Design Flow (FYP)		
XU, Zimo*	MW01b-22	Man WONG	Hardware implementation of neuromorphic circuits (FYP)	FYP	Shaneli
ZHANG, Zhejun					
CARRANZA MENJIVAR, Gabriel Emiliano	MW01c-22	Man WONG	Hardware implementation of neuromorphic circuits (FYP)	FYP	David
CHUNG, Wai Lok*	MWH01a-22	Wai Ho MOW	Image fusion of thermal and RGB-depth cameras and its applications (FYP)	FYP	Shaneli
CHOY, Yu Hin					
CHUI, Chi To Anson					
LI, Hoi Him*	MWH02b-22	Wai Ho MOW	Heat-sensitive paint for thermal camera calibration (FYP)	FYP	Shaneli
LI, Hong Yin					
LO, Yat Hei					
CHAN, Kwun Fung*	PA02a-22	Andrew POON	Self-proposed project - Apps Commanded Self-Routing Robot Waiter (FYP)	FYP	David
LEE, Yung Ching					
ZHANG, Juwen					
ZU, Haiyu*	PD01a-22	Daniel PEREZ PALOMAR	Trading volume modeling and prediction with time-series analysis techniques (FYP)	FYP	David
TIAN, Yifan					
YUEN, Chun Chung	PY01a-22	Patrick YUE	Millimeter-Wave Transmitter Circuits for Wireless Information and Power Transmission System (FYP)	FYP	David
KANG, Ting Hao Howard	QAA01a-22	Ahmed Abdul QUADEER	Deep learning models for guiding the design of peptide-based next generation vaccines (FYP)	FYP	Shaneli
LI, Wai Kit*	SK02a-22	Khawar SARFRAZ	I2C bus-controlled, microcontroller-based, fan tray design for temperature control of a server chassis (FYP)	FYP	David
LO, Lai Shun					
NG, Man Ho					
CHAN, Lik Chun*	SK02b-22	Khawar SARFRAZ	I2C bus-controlled, microcontroller-based, fan tray design for temperature control of a server chassis (FYP)	FYP	Shaneli
KWONG, Siu Chun					
YU, Shi Wang					
FUNG, Ho Ching*	SL02a-22	Ling SHI	Network communication and mobile application development on a locomotion robot (FYP)	FYP	David
LAM, Humi					
LUI, Nok					
KO, Yan Fai*	SL05a-22	Ling SHI	IoT smart tracking system (FYP)	FYP	Shaneli
SIU, Chi Chun					
WONG, Ho Lam					
DU, Xiawei*	SL05b-22	Ling SHI	UGVs Package Delivery (FYP)	FYP	David
LIU, Hanmo					
ZHENG, Zhinan					
WANG, Peiqi	SL05c-22	Ling SHI	AGVs Control on Objects Manipulation (FYT)	FYT	Shaneli
YANG, Jingwen*	SL05d-22	Ling SHI	UGVs Package Delivery (FYP)	FYP	David
JIA, Tongyao					

IKEMURA, Kei*	SL06a-22	Ling SHI	Robotic Guide Dog: A solution to the lack of guide dogs in societies (FYP)	FYP	Shaneli
ZHAO, Yu Xuan					
ZHANG, Jiekai					
SUTANTYA, Grace Angela*	SQ01a-22	Qiming SHAO	Embedded systems for AI hardware (FYP)	FYP	David
CHAN, Sze Chun					
RUSLI, Filbert					
CHOI, Inho	SQ02a-22	Qiming SHAO	EDA for superconducting quantum computing (FYT)	FYT	Shaneli
SON, Hangyul*	SS01a-22	Shenghui SONG	Development of Diversity Preserving Ensemble Distillation Algorithm for Federated Learning (FYP)	FYP	David
KIM, Sieun					
GUAN, Zhongyue	SS02a-22	Shenghui SONG	Self-proposed Project - Smart Construction Application for Monitoring Interior Furnishing with Metaverse) (FYP)	FYP	Shaneli
WONG, Cheuk Hei*	SSJ01a-22	Shaojie SHEN	A Swarm of UAVs (FYP)	FYP	David
CHUN, Ka Chung					
WONG, Ho Lun					
IP, Kwun Wang*	SSJ01b-22	Shaojie SHEN	A Swarm of UAVs (FYP)	FYP	Shaneli
LAM, Wing Leong					
SHEK, Wai Ho					
CHAN, Man Yin*	VL01a-22	Vincent Kin Nang LAU	Self-Proposed Project - Falling Motion Detection in Elderly Home (FYP)	FYP	David
HO, Chun Hin					
SIU, Cheuk Lok Shingo					
LI, Wai Chuen	VL01b-22	Vincent Kin Nang LAU	Self-Proposed Project - Smart Shoes With Perception (FYP)	FYP	Shaneli
TAM, Wing Hei*	VL02a-22	Vincent Kin Nang LAU	AI-Assisted PCB Prototyping using Laser Engraving (FYP)	FYP	David
TSUI, Kam Wing					
YIP, Wai Lok					
KWAN, Ho*	VL02b-22	Vincent Kin Nang LAU	AI-Assisted PCB Prototyping using Laser Engraving (FYP)	FYP	Shaneli
LEE, Chun Kit					
TAM, Cho Hin					
LAM, Kwong Pui*	WA01a-22	Kai Sun Albert WONG	Driver Assistance Vision System (FYP)	FYP	David
CHAN, Hei Yuk					
LEUNG, Ting Fung					
CHIU, Chun Hin*	WA02a-22	Kai Sun Albert WONG	AI Lifeguard (FYP)	FYP	Shaneli
KIANG, John					
KWOK, Ka Ho					
CHAN, To Yik*	WA03a-22	Kai Sun Albert WONG	Passenger Monitoring System (FYP)	FYP	David
VILLAREAL, Emanuel Jose					
LI, Chentian	WKT02a-22	Kam Tim WOO	Phase-to-phase Isolated DC to AC Inverter for Three-phase Unbalanced Load (FYP)	FYP	David
CHAN, Chi Choi*	WKT03a-22	Kam Tim WOO	IoT Solution for smart devices (FYP)	FYP	Shaneli
CHEUNG, Po Yu					

TSE, Chun Kiu					
FAN, Wai Lun*	WKT04a-22	Kam Tim WOO	IoT based indoor route design system (FYP)	FYP	David
CHEUNG, Kwan Ho					
WONG, Lap Man					
AU, Chun Ting*	WKT06a-22	Kam Tim WOO	Image inpainting and Completion Based on Deep Learning (YoloV3) and Convolutional Neural Network(CNN) (FYP)	FYP	Shaneli
HO, Sin Hang					
ZHAO, Chuwei					
IP, Hon Tung*	YW01a-22	Weichuan YU	Tissue Deformation Analysis Using Ultrasound Images (FYP)	FYP	David
WONG, Kar Wing					
LAI, Long Kan*	YW02a-22	Weichuan YU	PET Image Registration (FYP)	FYP	Shaneli
PANG, Kwan Lok					
WANG, Binghong	YY01a-22	Yamin YAN	Coordinated control of multi-vehicles (FYP)	FYP	David
HO, Tsz Fung	YYS01a-22	Yansong YANG	Microfabrication of radio frequency filter chips (FYP)	FYP	Shaneli
ZHENG, Yueyan*	YYS01b-22	Yansong YANG	Microfabrication of radio frequency filter chips (FYP)	FYP	David
SO, Huen Ling					
CHING, Cho Ho*	YYS01c-22	Yansong YANG	Microfabrication of radio frequency filter chips (FYP)	FYP	Shaneli
HUNG, Lok Tin					
CHIU, Long Chi*	YYS01d-22	Yansong YANG	Microfabrication of radio frequency filter chips (FYP)	FYP	David
HO, Kei Yuen					
CHIK, Chun Ming*	ZJ01a-22	Jun ZHANG	Edge AI for healthcare applications (FYP)	FYP	Shaneli
KOO, Sing					
HO, On Nam*	ZJ01b-22	Jun ZHANG	Edge AI for healthcare applications (FYP)	FYP	David
HON, Wang Chau					
ROH, Jaechul	ZJ02a-22	Jun ZHANG	Adversarial attacks to federated learning (FYT)	FYT	Shaneli
JUNG, Minwoo*	ZJ03a-22	Jun ZHANG	Multi-robot cooperative perception (FYP)	FYP	David
AGARWAL, Tanay					
SO, Yee Leuk					
LI, Zhengdong*	ZW01a-22	Wei ZHANG	Hardware Acceleration of Data Processing with FPGA or GPU (FYP)	FYP	Shaneli
LEE, Wai Tim					
CHENG, Wing Ching*	ZW01c-22	Wei ZHANG	Hardware Acceleration of Data Processing with FPGA or GPU (FYP)	FYP	Shaneli
CHUI, Yuk Ching					
CHAN, Ming Yat Gary*	ZW01d-22	Wei ZHANG	Hardware Acceleration of Data Processing with FPGA or GPU (FYP)	FYP	David
LUO, Qi Jun					
YE, Xiaojie					
CHONG, Wang Wa*	ZZ01a-22	Zheyang ZHENG	Design of a power electronic device (FYP)	FYP	Shaneli
YAU, Wai Kit					
CHAU, Tat Ching	ZZ02a-22	Zheyang ZHENG	Data analytics and integration of infrastructure data (Co-op)	Co-op	David